

In the Specification:

Please amend the paragraph beginning at page 6, line 16 of the application, as follows:

– The CAM core **120** is electrically coupled to a control circuit. The control circuit is illustrated as including a scheduler, a finite state machine and logic **110** that can support multiple overlapping contexts. The control circuit is further illustrated as including: a plurality of result mailboxes **90**, a result status register(s) **80**, a result status select register **70**, an interrupt indication circuit **60a** and a non-interrupt indication circuit **60b**. The result status register **80**, result status select register, interrupt indication circuit **60a** and non-interrupt indication circuit **60b** collectively define a result status notification circuit. –